



Digital Verification Engineer Semiconductor (all gender)

Munich

Reference Number: REF5078T

#weareALTEN - the home of forward thinkers

We are the designers of today for the world of tomorrow.

As one of the world's leading engineering service providers, we at ALTEN are committed to positively shaping the future of our partners, the careers of our employees and the challenges facing our society and environment.

More than 50,000 employees in 30 countries are already working on innovative solutions in various engineering and IT sectors such as automotive, aerospace, renewable energy, medical technology and railway technology.

Make it happen

YOU...

- verify mixed-signal designs on block and system level resting upon formal and functional verification
- interpret product requirements to create the verification concept and the product verification specification
- perform random-constraint testbenches according to the state of the art
- create assertions to prove the design using formal methods
- work in collaboration with lab and test engineers, designers, system architects, and verification engineers across multiple sites

Be our forward thinker

YOU...

- have completed engineering degree in electrical engineering, computer science or similar studies

- already have practical experience in the field of digital functional verification and common safety standards
- have extensive programming skills in coding in System Verilog and System Verilog assertions
- have good knowledge of UVM methodology or Specman - e language, scripting languages like Python or Perl and ability to interpret RTL and Gatelevel code
- have good communication skills in English

We value diversity and therefore welcome all applications - regardless of gender, nationality, ethnic and social origin, religion/belief, disability, age and sexual orientation and identity.

Severely disabled persons will be given preferential consideration if they are equally qualified.

Do you have any questions?

Then contact me: Özlem Mumin (Tel.: +49 89 255552-331)

Or apply now using our online application form.

A home where people learn and thrive

- **Talent Management** - we develop your career
- **Work life balance** - flexible working hours and mobile working possible
- **Fit and relaxed** – with EGYM Wellpass
- **Enjoy biking** – always on tour with bike leasing
- **Green Mobility** - with us you can travel at a reduced rate

... and of course we offer the usual standards such as 28 days holiday, many events, permanent employment contract and corporate benefits.

Your contact person

ALTEN GmbH
Özlem
MUMIN

karriere@alten.com

631efc5e14b46e310ce06d93
Özlem MUMIN

Created: 09.05.2024, <https://www.alten-germany.de/en/jobs/743999983372023-digital-verification-engineer-semiconductor-all-gender/>