

Digital Design Engineer Semiconductor (all gender)

Munich

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#weareALTEN - the home of forward thinkers

We are the designers of today for the world of tomorrow.

As one of the world's leading engineering service providers, we at ALTEN are committed to positively shaping the future of our partners, the careers of our employees and the challenges facing our society and environment.

More than 50,000 employees in 30 countries are already working on innovative solutions in various engineering and IT sectors such as automotive, aerospace, renewable energy, medical technology and railway technology.

Make it happen

YOU...

- develop SystemVerilog code to implement the required digital functionality
- adapt and enhance existing IP modules to support new features and requirements
- · set up and execute logic synthesis, including constraint management
- perform implementation verification using Spyglass and Logic Equivalence Checking (LEC)
- conduct power simulations and analyze results
- Prepare comprehensive design documentation.

Be our forward thinker

YOU...

- have completed degree in Electrical Engineering, Computer Science, Physics or similar studies
- have proven experience in RTL design and verification (SystemVerilog, VHDL)
- have a strong background in logic synthesis, timing analysis, and power simulation

- have experience with industry-standard tools: Spyglass, DesignCompiler, LEC, Xcelium, ATPG
- have good communication skills in English

We value diversity and therefore welcome all applications - regardless of gender, nationality, ethnic and social origin, religion/belief, disability, age and sexual orientation and identity. Severely disabled persons will be given preferential consideration if they are equally qualified.

Do you have any questions?

Then contact me: Özlem Mumin (Tel.: +49 89 255552-331)

Or apply now using our online application form.

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...in addition we offer a permanent employment contract, corporate benefits and team events.

Your contact person

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